

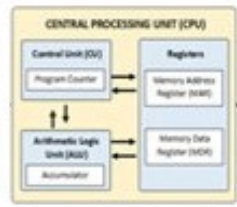
Unit 1: Computer Systems



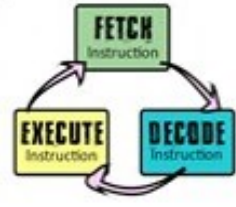
Von Neumann



CPU



CPU Architecture



Fetch Decode Execute Cycle



Non-Volatile ROM



Volatile RAM

Secondary Storage



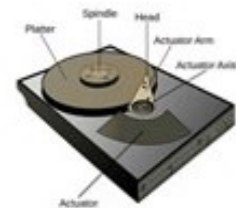
Optical Storage



Universal Serial Bus



Solid-state storage



Magnetic Hard Disks

Logic Gates



Input	Output
1	0
0	1
1	0



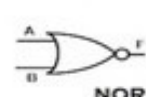
Inputs	Output	
A	B	F
0	0	1
1	0	0
0	1	0
1	1	0



Inputs	Output	
A	B	F
0	0	0
1	0	0
0	1	0
1	1	1



Inputs	Output	
A	B	F
0	0	0
1	0	1
0	1	1
1	1	1



Inputs	Output	
A	B	F
0	0	1
1	0	0
0	1	0
1	1	0

System Software



Operating Systems



Software



Disk Management

Devices



Input Mouse



Input / Output Headset

Embedded Systems



Washing Machine



Microwave



Main Memory



Secondary Storage



Logic Gates



RAM & ROM



Embedded Systems



```
1 print('Hello, Welcome to Unit 1: Computer Systems !')
```

Hello, Welcome to Unit 1: Computer Systems !